

Announcement

Z CIRCUIT releases its 2nd generation Library Analyzer Validation and Analysis system

Extensive suite of tools provide automatic validation and versatile analysis of increasingly complex cell library model technology

Mountain View, California, July 14, 2009 – Z Circuit, the leading provider of library validation and analysis tools, today announced that a 2nd generation suite of tools that adds rule-based validation to the existing compare validation, analysis, and mapping tools.

Z Circuit pioneered library analysis and validation tools with the release of Library Analyzer in 2005. As cell models started to rapidly increase in complexity, library files increased to more than 100x in size since 2005 and the number of corners has rapidly expanded. Low-power design methods and timing accuracy problems since 90nm technology now require as many as 20 corners for the new 40nm and 28 nm technologies.

Companies producing next-generation integrated circuit designs have a need to have accurate cell library models for their entire digital IP portfolio including standard cells, memories, and IO pads. Companies who create libraries need tools to validate and analyze all library components including standard cells, memories and IO pads. In addition, companies who use libraries often produce some of their own cells or need to qualify libraries to minimize their risk of using libraries that have errors.

Many existing cell library characterization systems have not been able to scale with the increasing data complexity demands required at the current process geometries and the risk of introducing errors into the production libraries has greatly increased.

Z Circuit's **Library Analyzer** validation tools are critical software systems specifically designed to scale with these increasing complexity demands.

"Rule-base validation in Library Analyzer is a powerful new method to allow designers to verify library correctness" said Edmond Macaluso, President of Z Circuit Automation. "Not only does it support hundreds of built-in checks, but users can also define their own rule sets. In addition, rule-based validation is an excellent compliment to the existing compare-based methods already provided in Library Analyzer."

About Z Circuit

Z Circuit develops and markets Electronic Design Automation (EDA) software and provides a unique all-in-one solution for standard cell, memory, and IO pad cell library characterization and validation.

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